

**Notice of References Cited**

Application/Control No.

09/980,754

Applicant(s)/Patent Under  
Reexamination  
LERCH ET AL.

Examiner

Julio J. Maldonado

Art Unit

2823

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	C	US-6,319,779 B1	11-2001	Ahmad et al.	438/275
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**NON-PATENT DOCUMENTS**

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	U	Wolf, Stanley and Tauber, Richard N.; Silicon processing for the VLSI Era Vol 1: Process Technology; 1986, Lattice press, pgs.192-194.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.